

## SH67/69 HA MCU(家電单片机)selection table---LCD series

Date: 2007/10/10

## LCD OTP MCU

	OTP ROM (16-bit)	RAM (4-bit)	Voltage (V)	CPU clock (Hz)	I/O	LCD	Interrupt source	Timer	Stack (level)	Op- current	Halt current	Stop current	ADC	PWM	WDT	LVR	Others	PKG pins	PKG Type
SH66P51	2048	128	1.8~3.6	Dual Clock 32K-4M	18 (8+10)	3x29~ 6x26	T0,BT PBC	2	4	300uA 4M	4uA 32k	<1uA	--	--	Yes	1.7V	2% internal crystal,lcd regulator, LPD	64	Chip LQFP
SH66P52	2048	128	2.4-5.5	Dual Clock 32K-4M	8+4LCD	4x16	ADC,T0, BT,PB	2	4	600uA 4.19M	5uA 32K	<1uA	16bit	--	Yes	2.4V	16Bit Σ-ΔADC	52	Chip
SH67P53	4096	176	2.4-6	Dual Clock 32K-8M	12IO+ 15out	4x20	Ext, T0, BT, PBC	2	4	22uA 32K	7uA/32K 5V	<1uA	--	--	Yes	2.5V 4V	PSG	52 pads	Chip, QFP52
SH67P54	4096	336	2.4-6	Dual Clock 32K-8M	16IO+ 26out	4,5,6 x 34	Ext, T0, BT, PBC	2	8	22uA 32K	7uA/32K 5V	<1uA	--	--	Yes	2.5V 4V	PSG	58 pads	Chip, QFP64
SH69P561	4096	274	2.4-5.5	Dual Clock 32K-8M	15 (11+4)	4x18	ADC,T0, T1,PB	2	8	1.5mA 8M	500uA 8M	<1uA	8bit 5ch	10bit 2ch	Yes	2.5V 4V	EL, LED, Alarm, ZCD,	44	QFP44
SH69P56	4096	274	2.4-5.5	Dual Clock 32K-8M	20 (12+8)	3x31~ 6x28	ADC,T0, T1,PB	2	8	1.5mA 8M	500uA 8M	<1uA	8bit 6ch	10bit 2ch	Yes	2.5V 4V	EL, LED, Alarm, ZCD,	64	Chip QFP64
SH69P551	8192	512	2.4-5.5	Dual Clock 32K-8M	26	6x8	T0,T1,T2, PBC,ADC ,Keyscan	3	8	1.5mA 8M	500uA 8M	<1uA	10bit 4ch	10bit 1ch	Yes	2.5V 4V	PLL,PFG, LED driver	28	Chip SKDIP SOP
SH69P55	8192	512	2.4-5.5	Dual Clock 32K-8M	42 (12+8)	4x20 6x18 8x16	T0,T1,T2, PBC,ADC ,Keyscan	3	8	1.5mA 8M	500uA 8M	<1uA	10bit 8ch	10bit 2ch	Yes	2.5V 4V	PLL,PFG, LED driver	44 42 32	QFP44 SDIP42 DIP32 SOP28
SH67P93	24k	5.6k	2.4-3.6	Dual Clock 32K- 3.58M	8+15IO+ 22output	16x50 8x50	T0,T1 PBCD	2	8	10uA 32K	6uA/32K 3V	<1uA	--	--	Yes	--	PLL,DTMF/FS K	92 pads	Chip, QFP100